

Amendments to the Claims:

This listing of claims replaces all prior versions, and listings, of claims of this application:

Listing of Claims:

1. (Original) A method of programming an electronic device comprising:
transferring program data from outside the electronic device to a
programmable memory in the electronic device via a controller circuit that controls
programming of the programmable memory in the electronic device that is separate
from a general operation processor circuit used to provide general operations of the
electronic device subsequent to transferring the program data into the programmable
memory.
2. (Original) A method according to Claim 1 wherein the step of
transferring further comprises:
transferring the program data without using Random Access Memory (RAM)
and Read Only Memory (ROM) devices, that are separate from the controller circuit.
3. (Original) A method according to Claim 2 wherein the RAM and
ROM operate under control of the general operation processor circuit and not under
the control of the controller circuit.
4. (Original) A method according to Claim 1 wherein the step of
transferring the program data further comprises transferring the program data via a
Video Graphics Adapter (VGA) interface to the electronic device.
5. (Original) A method according to Claim 1 wherein the step of
transferring the program data further comprises transferring the program data via an
Inter-Integrated Circuit interface to the electronic device.

6. (Original) A method according to Claim 1 wherein the general operation processor circuit accesses the program data in the programmable memory to provide the general operations of the electronic device subsequent to transferring the program data into the programmable memory.

7. (Original) A method according to Claim 6 wherein the general operation processor circuit accesses separate RAM and ROM to provide general operations of the electronic device.

8. (Original) A method according to Claim 1 further comprising:
preventing the general operation processor circuit from accessing the programmable memory during the transfer of program data into the programmable memory.

9. (Original) A method according to Claim 1 further comprising:
transmitting an indication to outside the electronic device that the transfer of program data to the programmable memory is complete.

10. (Original) A method according to Claim 9 further comprising:
transmitting a reset to the controller circuit to enable the general operation processor circuit to access the program data transferred into the programmable memory.

Claims 11-19 (Canceled).

20. (Original) A circuit for programming a monitor comprising:
a controller circuit configured to transfer program data from outside the monitor to a programmable memory in the monitor, the controller circuit being separate from a general operation processor circuit used to provide general operations of the monitor subsequent to transferring the program data into the programmable memory.

21. (Original) A circuit according to Claim 20 wherein the controller circuit comprises:

a decoder circuit coupled to an interface via which the program data is transferred to the monitor, the decoder circuit configured to provide a first signal responsive to determining that data received via the interface includes an address within a controller circuit address range; and

a programmable memory controller coupled to the decoder circuit configured to provide at least one control signal to the programmable memory responsive to the first signal.

22. (Original) A circuit according to Claim 21 further comprising:

a buffer circuit coupled to the decoder circuit and the programmable memory and configured to provide data to/from the programmable memory.

Claim 23 (Canceled).

24. (Previously presented) A method according to Claim 1 wherein the step of transferring the program data further comprises transferring the program data via a Serial Interface to the electronic device.

25. (Previously presented) A method according to Claim 9 further comprising:

recycling power provided to the controller circuit to reset controller circuit to enable the general operation processor circuit to access the program data transferred into the programmable memory.

Claim 26 (Canceled).